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Ekanadham, K.; Beng-Hong Lim; Pattnaik, P.; Snir, M.;

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Chen-Chi Kuo; Carter, J.; Kuramkote, R.; Swanson, M.; Parallel Processing, 1998. Proceedings. 1998 International Conference on 10-14 Aug. 1998 Page(s):207 - 216 Digital Object Identifier 10.1109/ICPP.1998.708488

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Mumolo, E.; Bernardis, G.; System Sciences, 1997, Proceedings of the Thirtieth Hawaii International Conference on Volume 5, 7-10 Jan. 1997 Page(s):160 - 167 vol.5 Digital Object Identifier 10.1109/HICSS.1997.663171

[AbstractPlus](#) | Full Text: [PDF\(684 KB\)](#) [IEEE CNF](#)**3. PRISM: an integrated architecture for scalable shared memory**

Ekanadham, K.; Beng-Hong Lim; Pattnaik, P.; Snir, M.; High-Performance Computer Architecture, 1998. Proceedings., 1998 Fourth International Symposium on 1-4 Feb. 1998 Page(s):140 - 151 Digital Object Identifier 10.1109/HPCA.1998.650554

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Nikolopoulos, D.S.; Polychronopoulos, C.D.; Cluster Computing and the Grid 2nd IEEE/ACM International Symposium CCGRID2002 21-24 May 2002 Page(s):14 - 21 Digital Object Identifier 10.1109/CCGRID.2002.1017108

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Weiwu Hu; Weisong Shi; Zhimin Tang; High Performance Distributed Computing, 1999. Proceedings. The Eighth International Symposium on 3-6 Aug. 1999 Page(s):353 - 354

 7. **Adaptive Scheduling under Memory Pressure on Multiprogrammed Clusters**

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[AbstractPlus](#) | Full Text: PDF(1400 KB) IEEE JNL

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IEEE JNL IEEE Journal or Magazine

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IEEE CNF IEEE Conference Proceeding

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IEEE STD IEEE Standard

 29. **A 4-Mbit DRAM with trench-transistor cell**

Shah, A.H.; Wang, C.; Womack, R.H.; Gallia, J.D.; Shichijo, H.; Davis, H.E.; Elahy, M.; Banerjee, S.K.; Pollack, G.P.; Richardson, W.F.; Bordelon, D.M.; Malhi, S.D.S.; Pilch, C.J.; Tran, B.; Chatterjee, P.K.;

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